

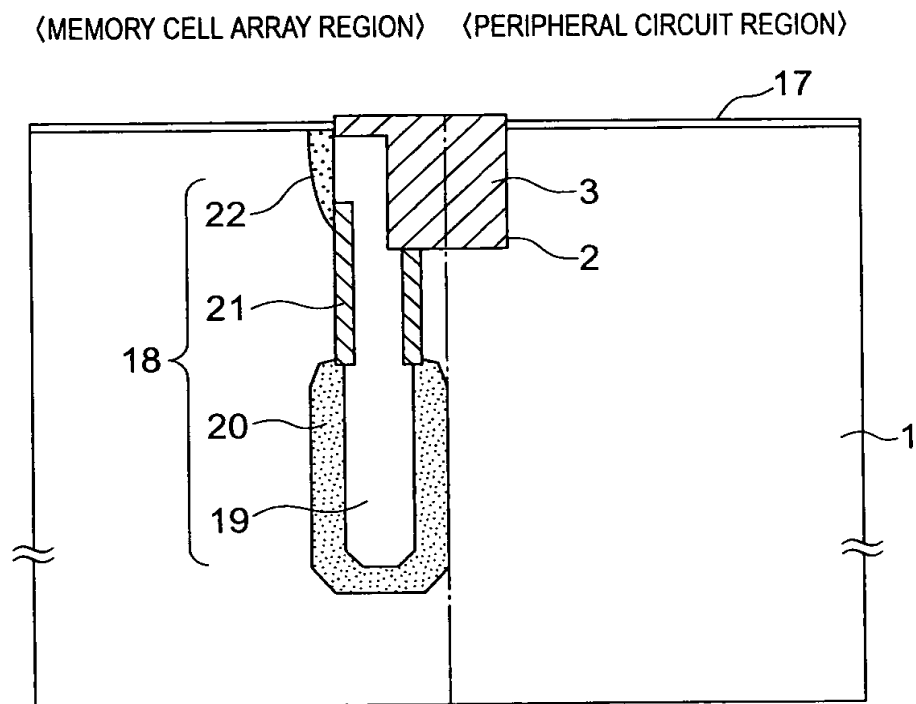


FIG. 1

〈MEMORY CELL ARRAY REGION〉 〈PERIPHERAL CIRCUIT REGION〉

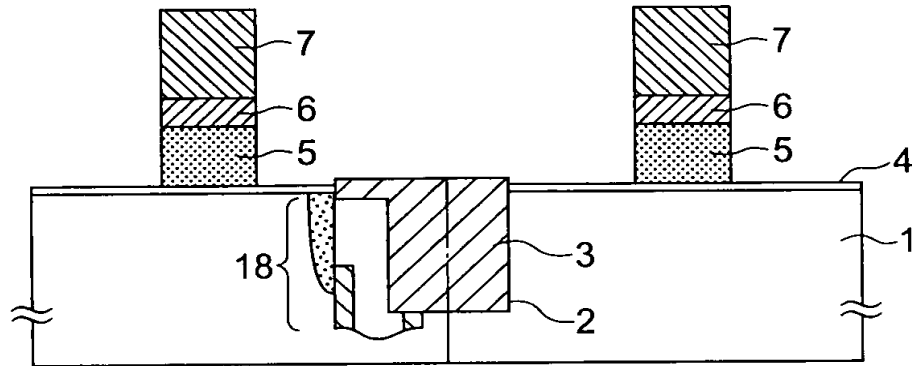


FIG. 2

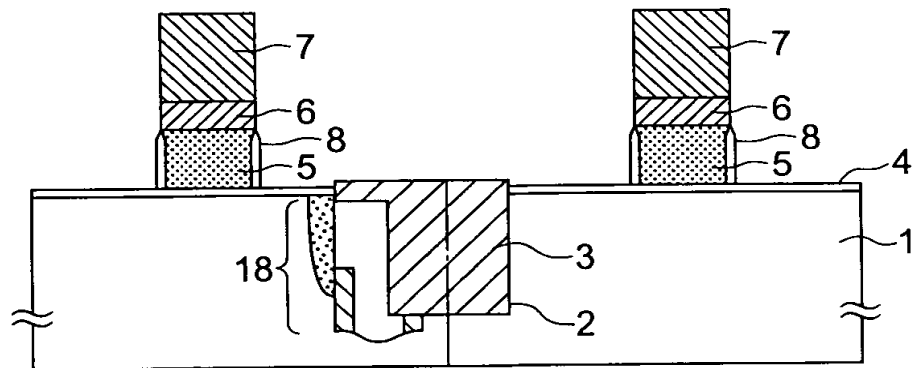


FIG. 3

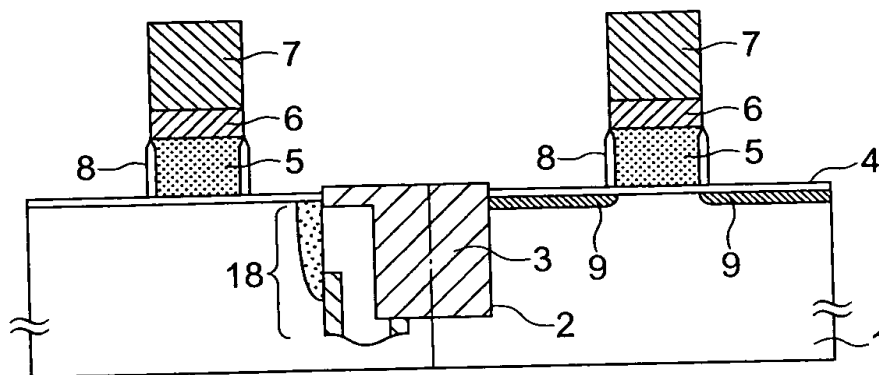


FIG. 4

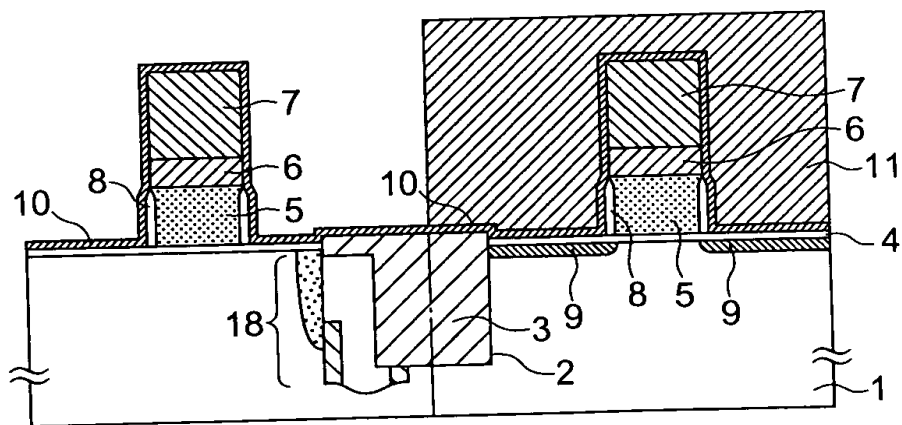


FIG. 5

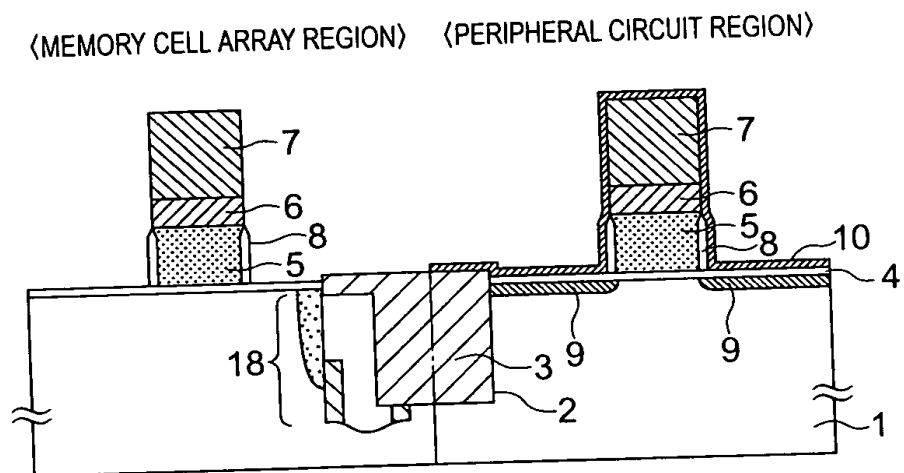


FIG. 6

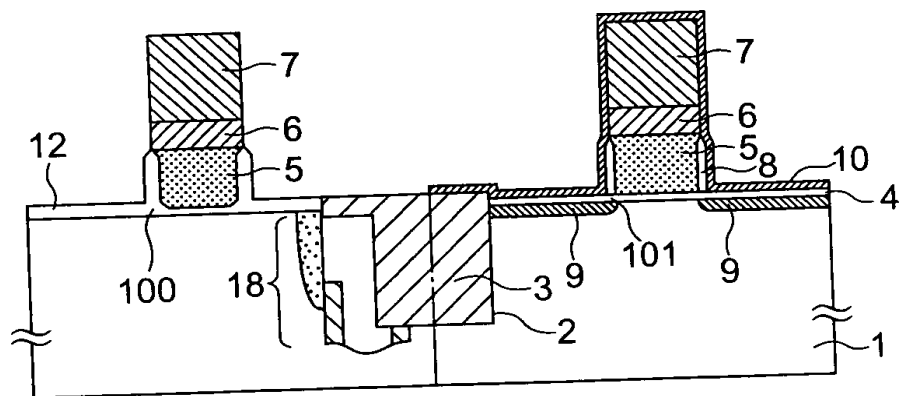


FIG. 7

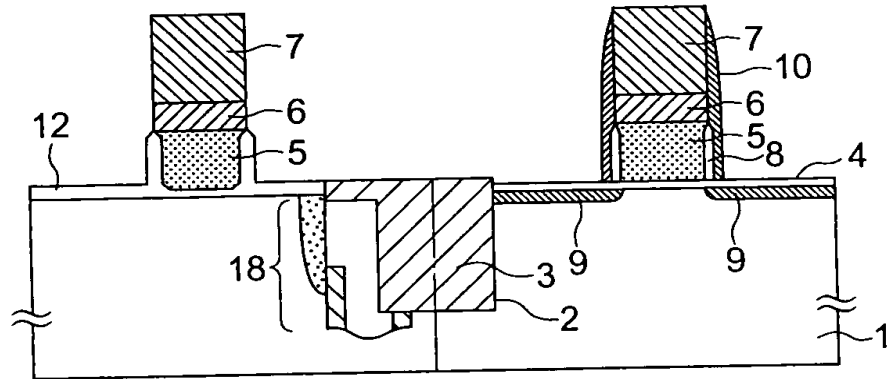


FIG. 8

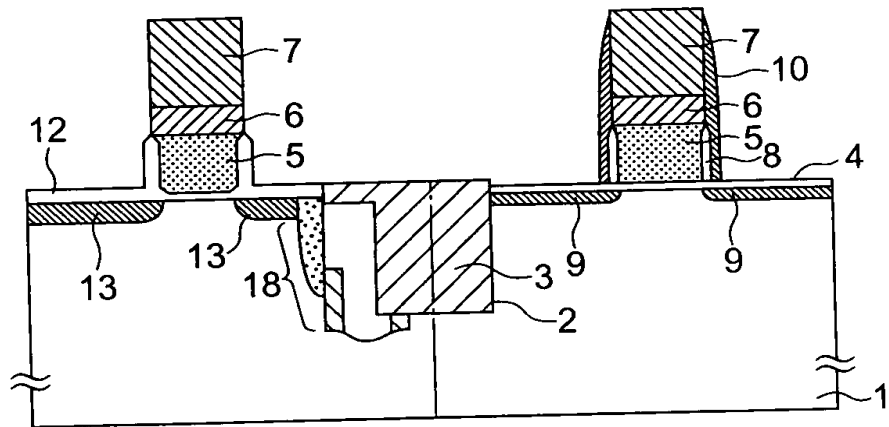


FIG. 9

〈MEMORY CELL ARRAY REGION〉 〈PERIPHERAL CIRCUIT REGION〉

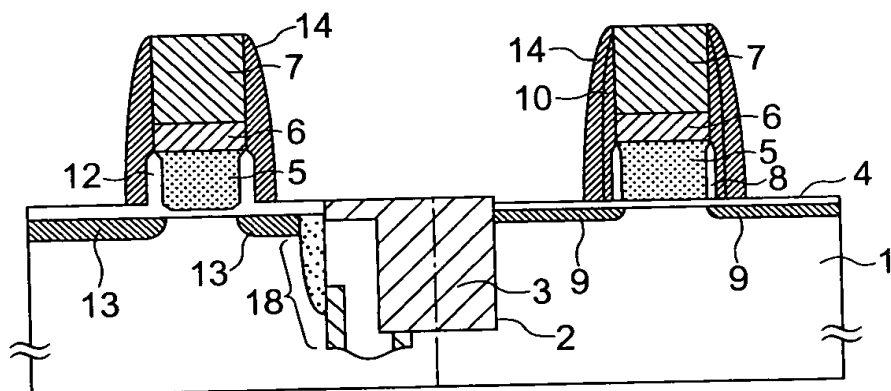


FIG. 10

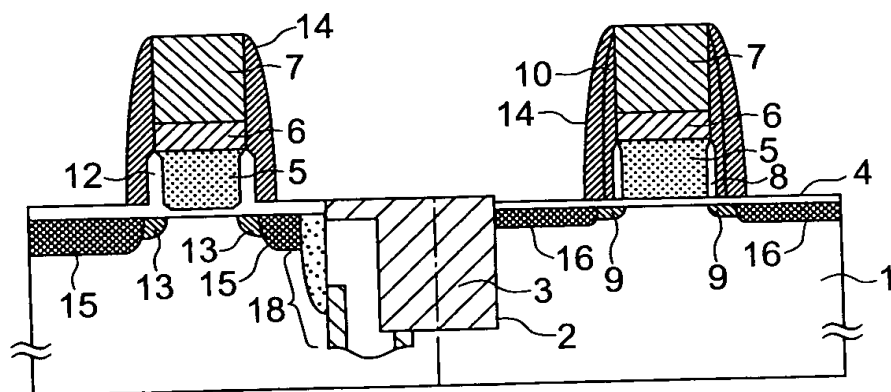


FIG. 11

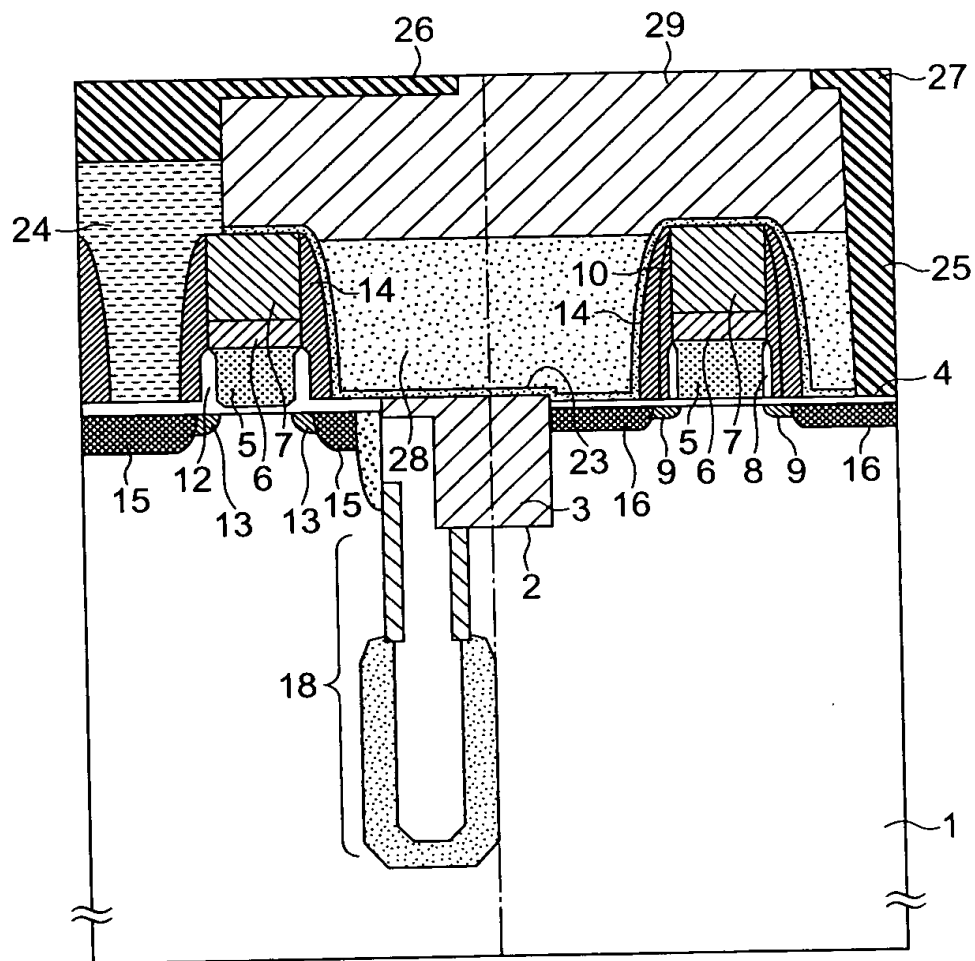


FIG. 12

〈MEMORY CELL ARRAY REGION〉 〈PERIPHERAL CIRCUIT REGION〉

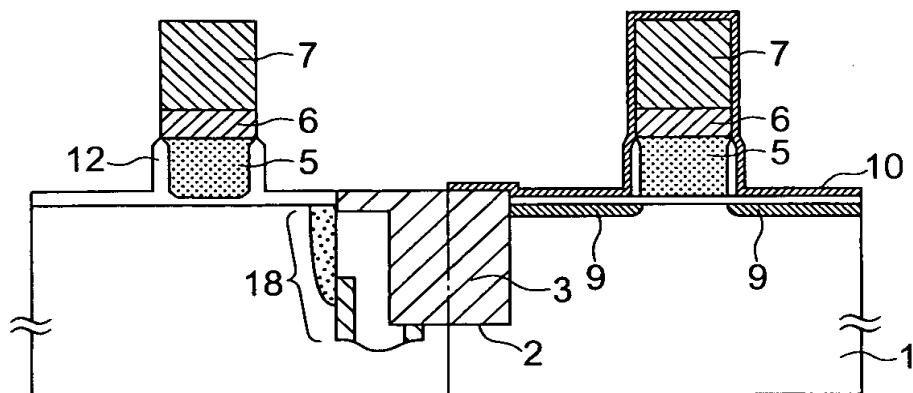


FIG. 13

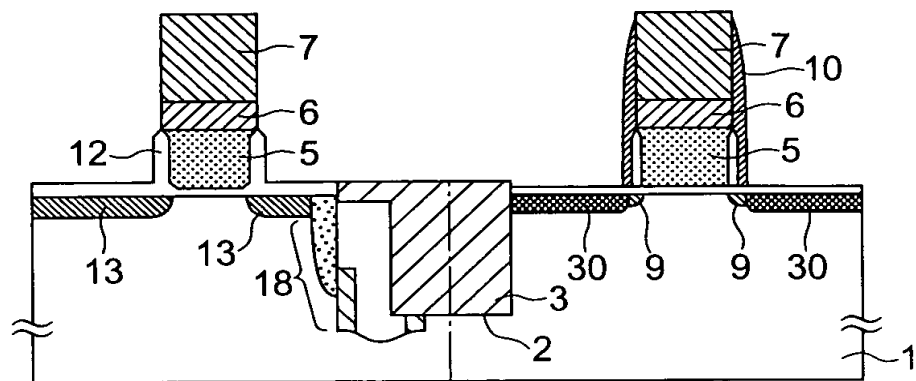


FIG. 14

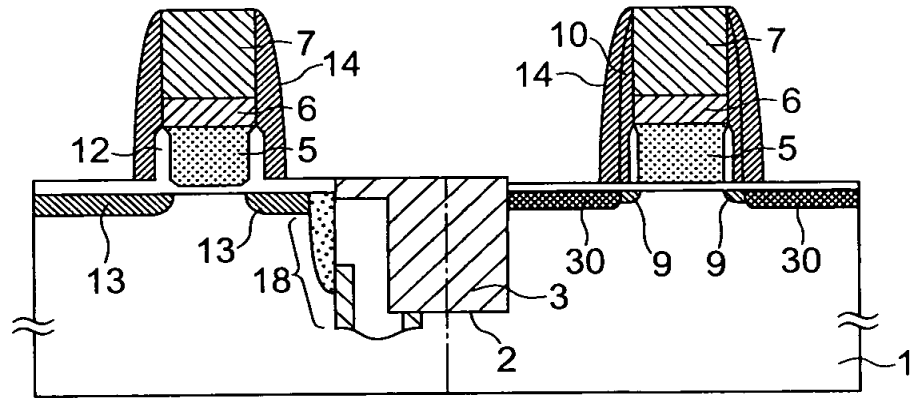


FIG. 15

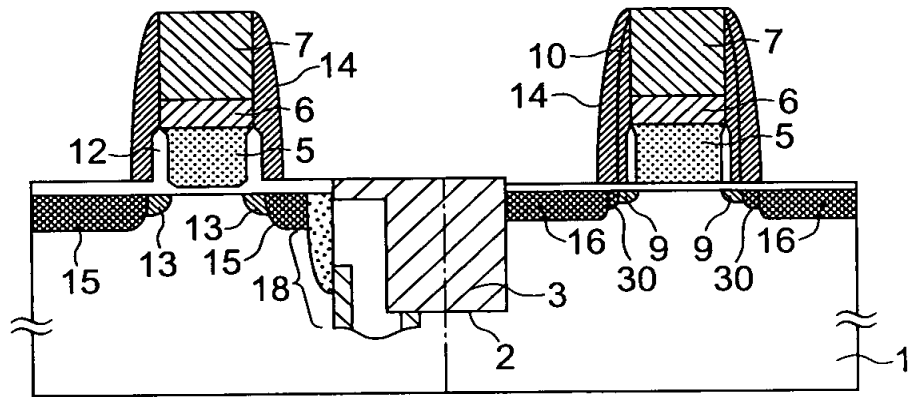


FIG. 16



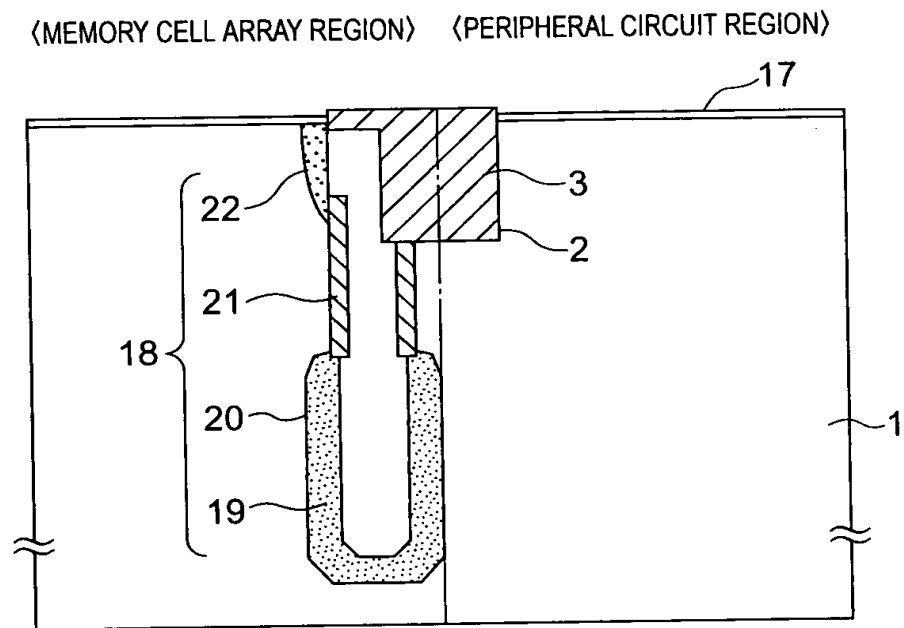


FIG. 18

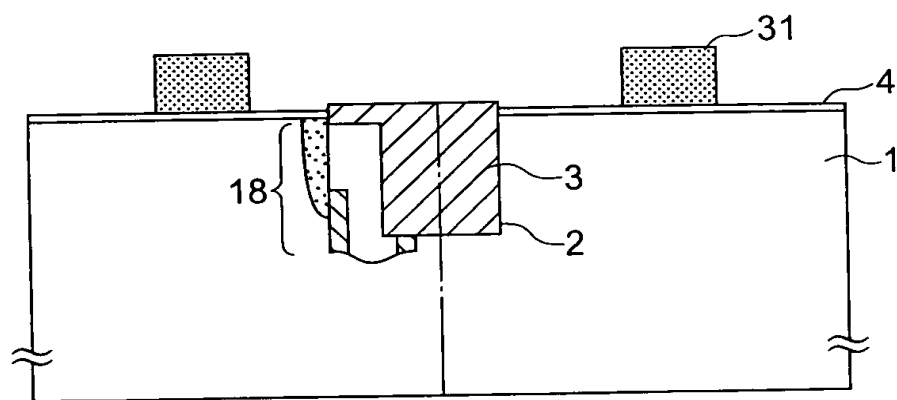


FIG. 19

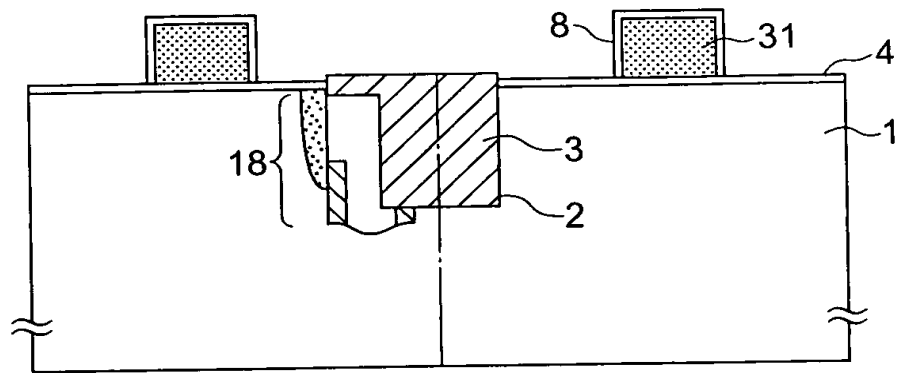


FIG. 20

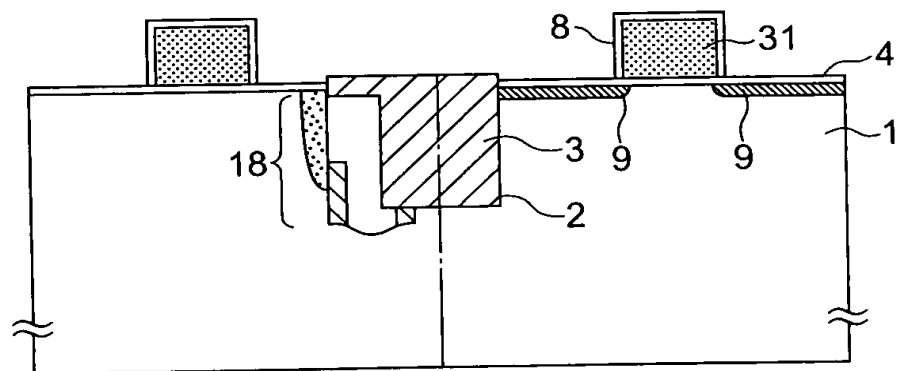


FIG. 21

〈MEMORY CELL ARRAY REGION〉 〈PERIPHERAL CIRCUIT REGION〉

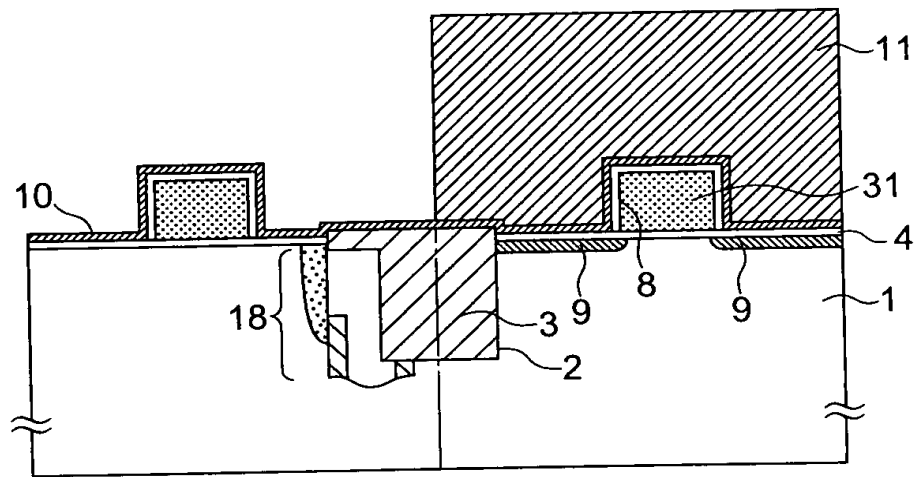


FIG. 22

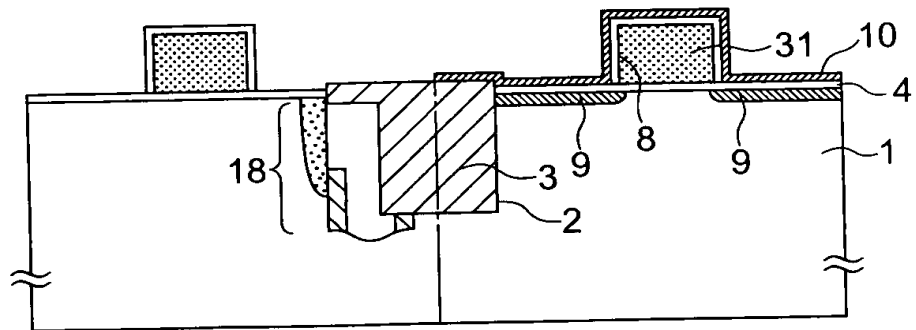


FIG. 23

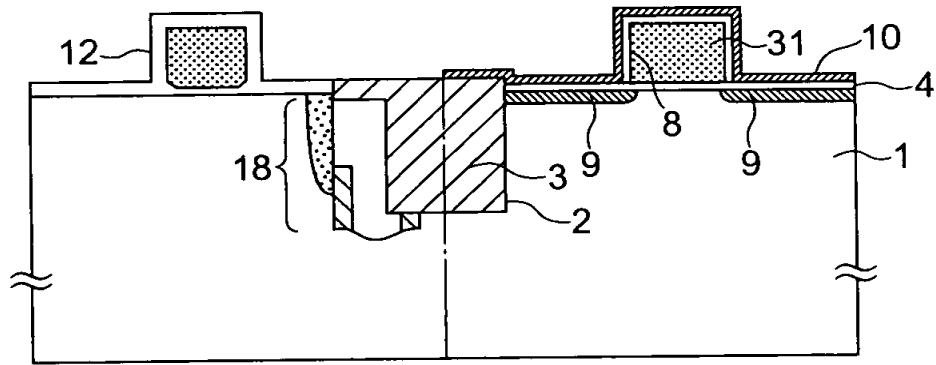


FIG. 24

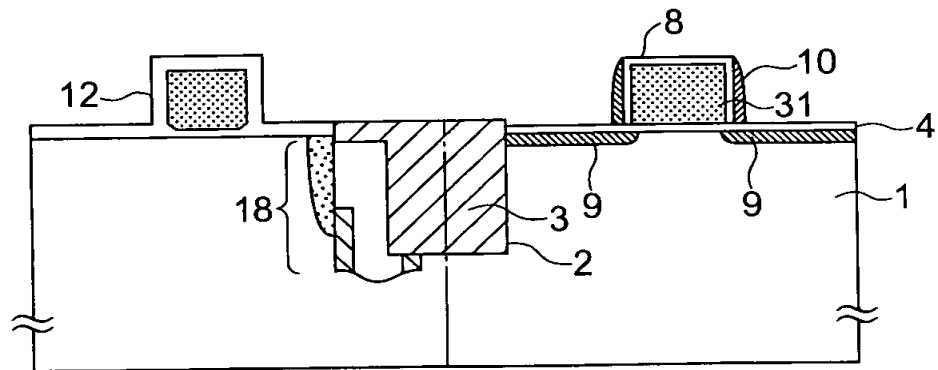


FIG. 25

〈MEMORY CELL ARRAY REGION〉 〈PERIPHERAL CIRCUIT REGION〉

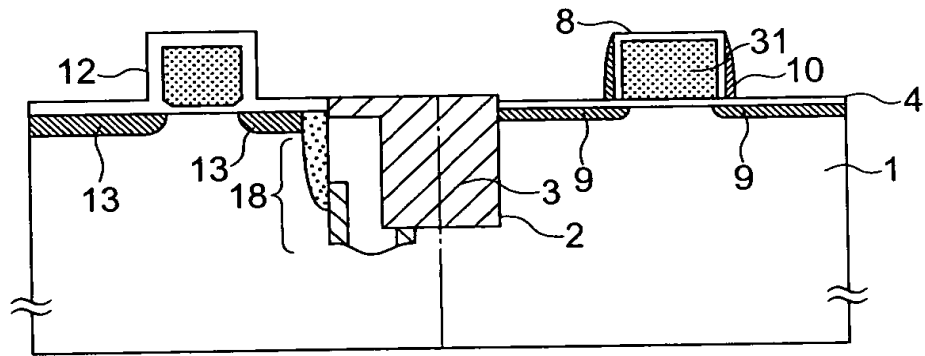


FIG. 26

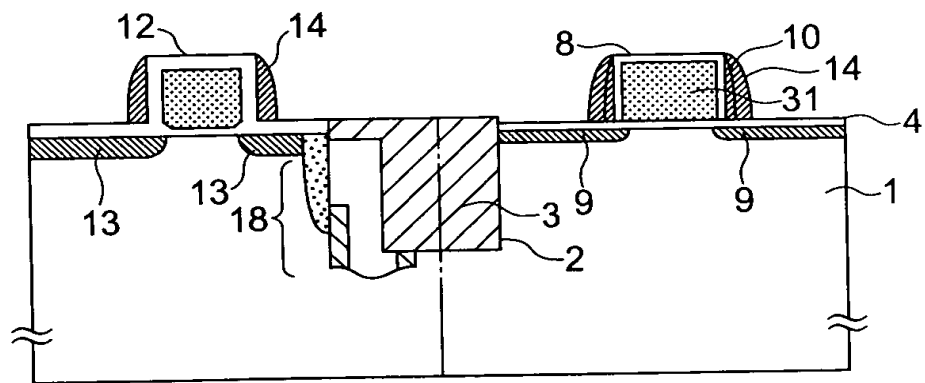


FIG. 27

This cross-sectional view shows a central block 2 with a top surface 3. To the left of block 2 is a region 12, and to the right is a region 10. Both regions 12 and 10 have a top surface 14. A layer 15 is formed on the top surface 14 of region 12 and the top surface 14 of region 10. A layer 16 is formed on the top surface 14 of region 10. A layer 13 is formed on the top surface 14 of region 12. A layer 18 is formed on the top surface 14 of region 12. A layer 9 is formed on the top surface 14 of region 10. A layer 8 is formed on the top surface 14 of region 10. A layer 31 is formed on the top surface 14 of region 10. A layer 13 is formed on the top surface 14 of region 12. A layer 15 is formed on the top surface 14 of region 12. A layer 16 is formed on the top surface 14 of region 10. A layer 9 is formed on the top surface 14 of region 10. A layer 8 is formed on the top surface 14 of region 10. A layer 31 is formed on the top surface 14 of region 10.

FIG. 29

〈MEMORY CELL ARRAY REGION〉 〈PERIPHERAL CIRCUIT REGION〉

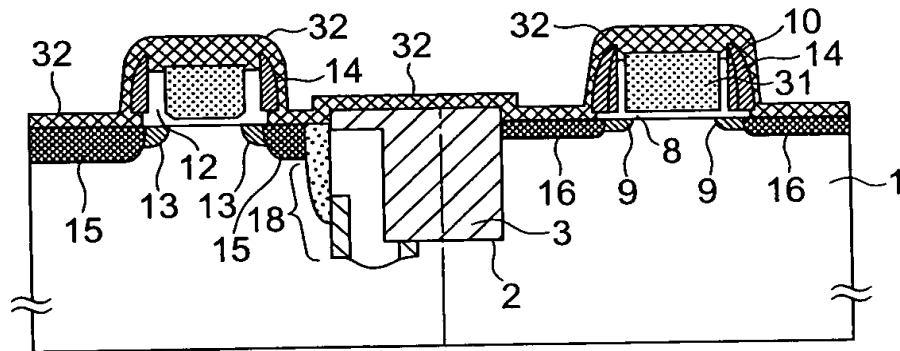


FIG. 30

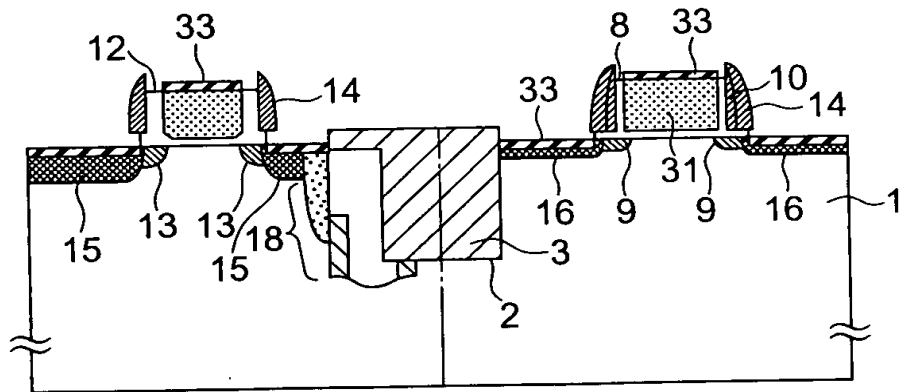


FIG. 31

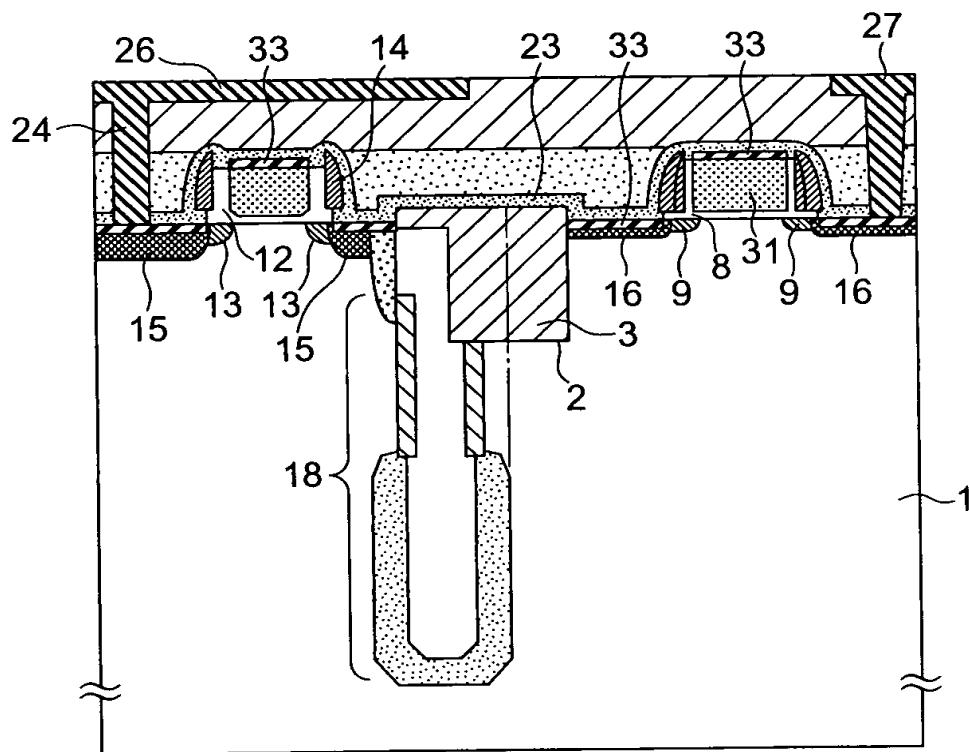


FIG. 32

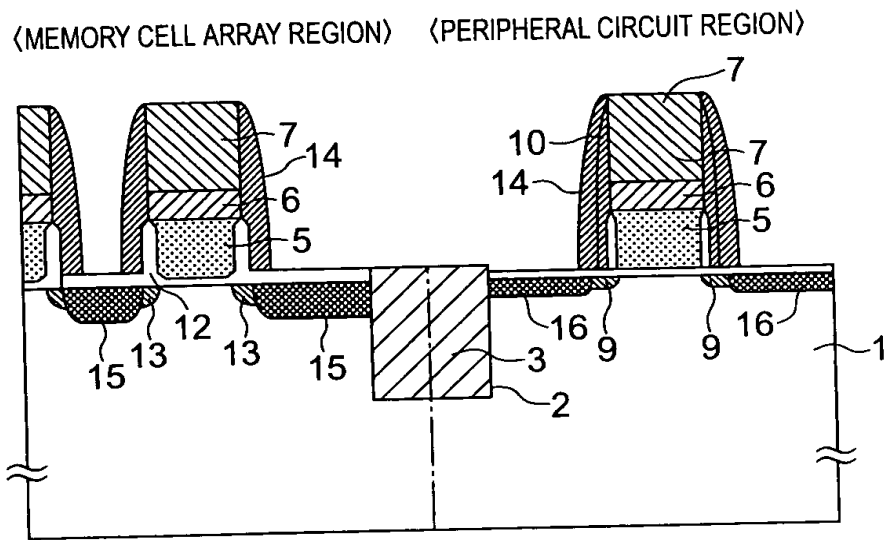


FIG. 33

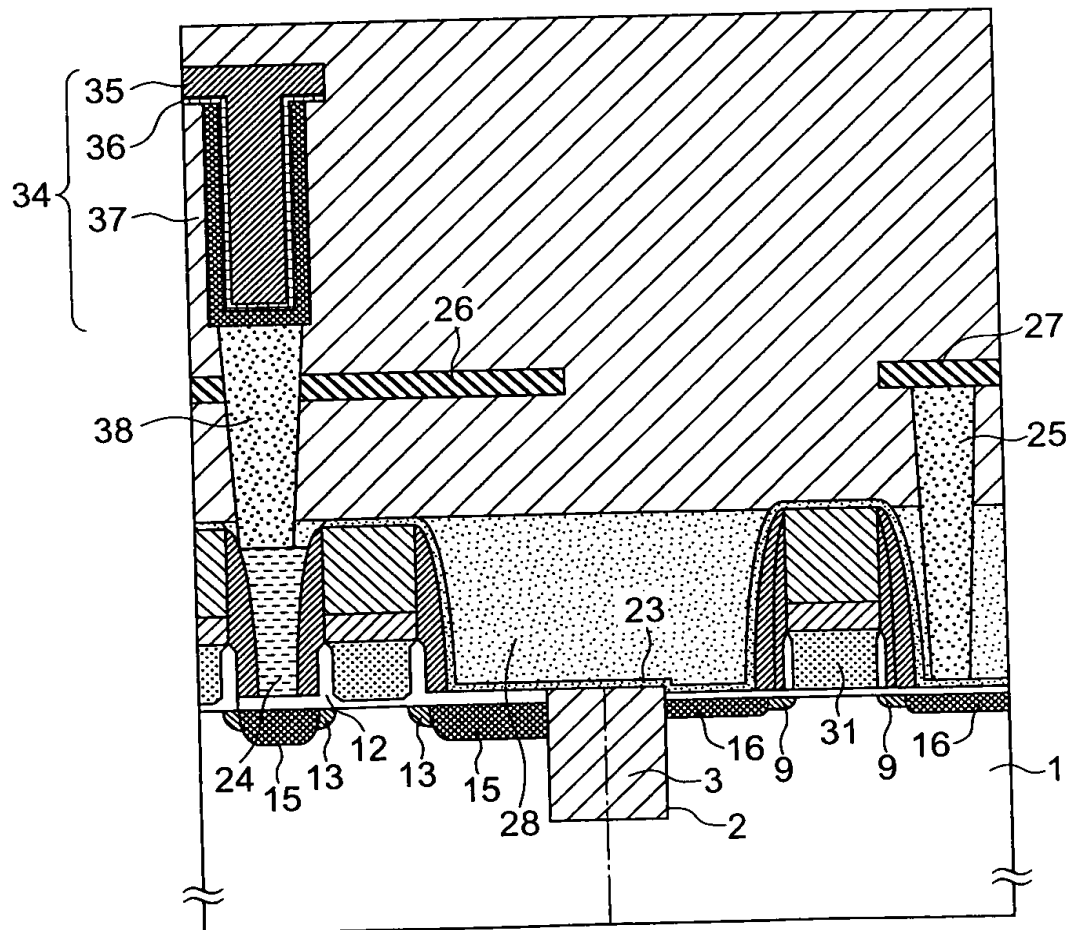


FIG. 34

〈MEMORY CELL ARRAY REGION〉 〈PERIPHERAL CIRCUIT REGION〉

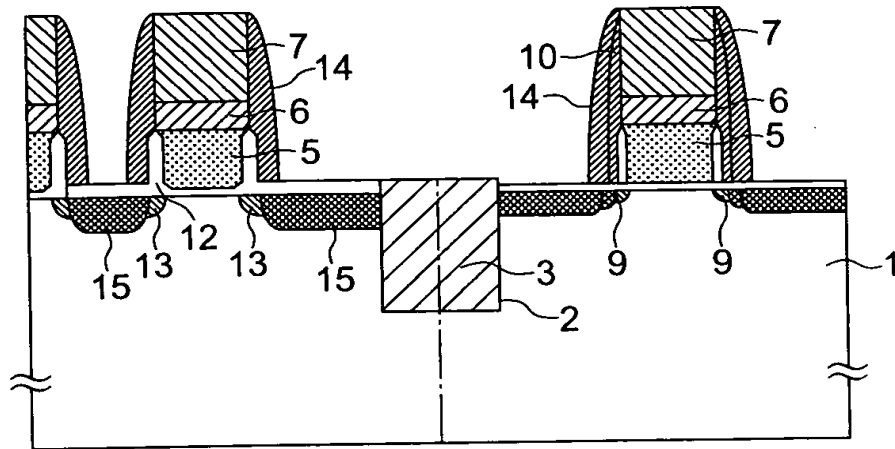


FIG. 35

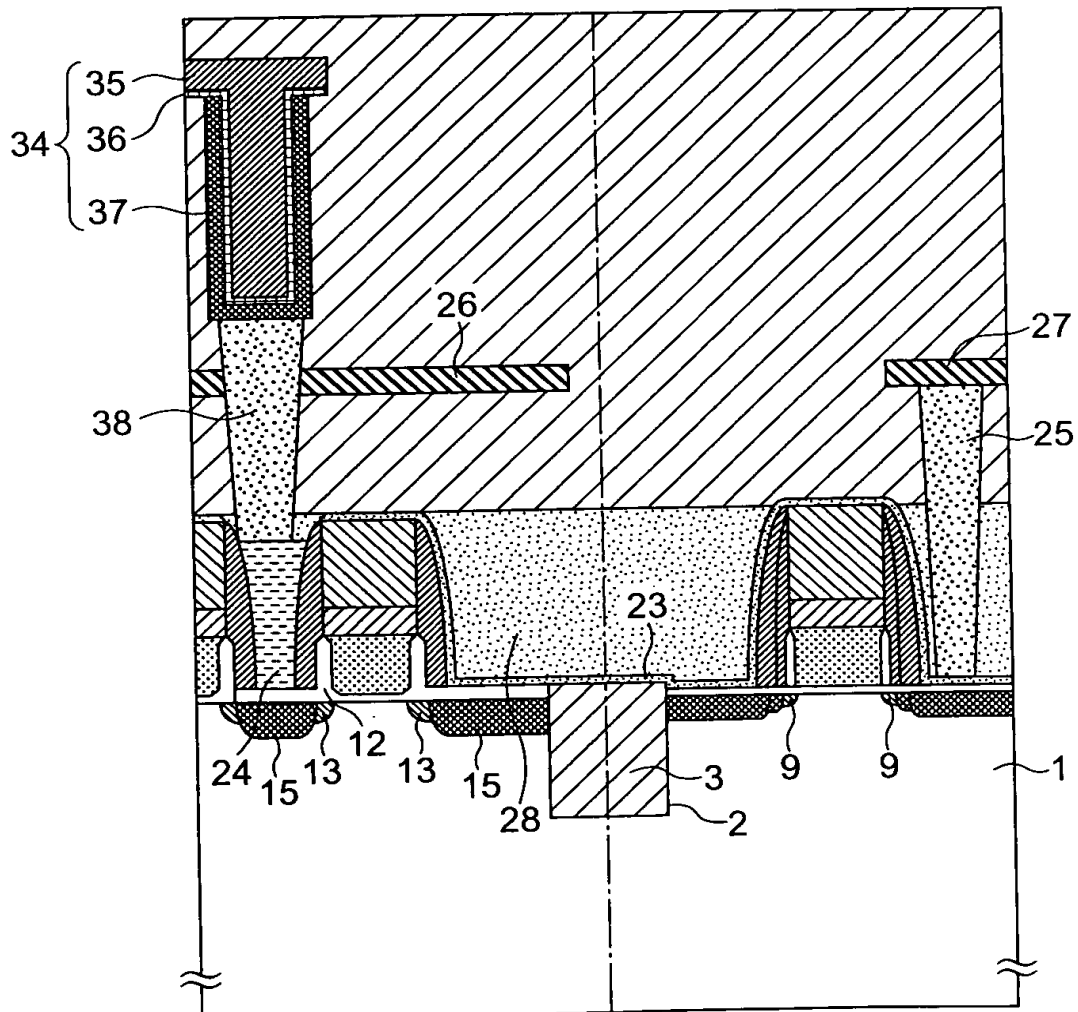


FIG. 36

〈MEMORY CELL ARRAY REGION〉 〈PERIPHERAL CIRCUIT REGION〉

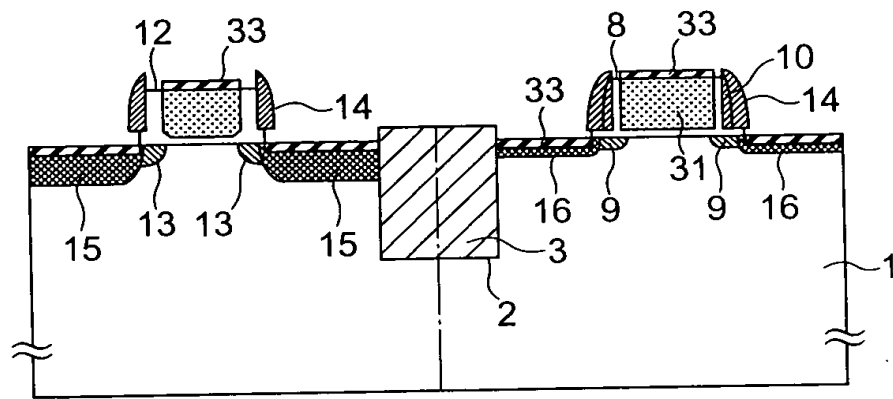


FIG. 37

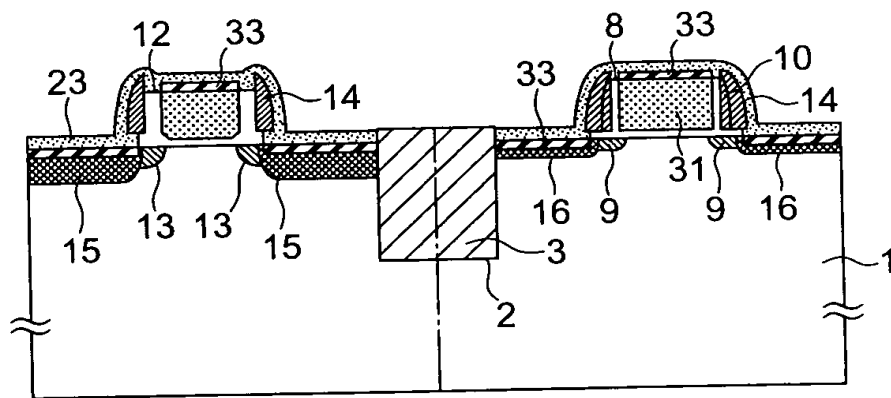


FIG. 38



FIG. 39